

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	6577	((cut turn switch\$3) adj off) near4 (backup battery auxiliary main)	USPAT	OR	OFF	2004/11/04 14:03
L2	8149	(self-refresh refresh) near4 (memory ram Dram)	USPAT	OR	OFF	2004/11/04 14:04
L3	1	I1 with I2	USPAT	OR	OFF	2004/11/04 14:05
L4	57	I1 and I2	USPAT	OR	OFF	2004/11/04 14:06

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	32116	battery near5 switch\$3	USPAT	OR	OFF	2004/11/03 16:22
S2	676	S1 with memory	USPAT	OR	OFF	2004/11/03 15:30
S3	368	S2 with power	USPAT	OR	OFF	2004/11/03 15:27
S4	6	S3 with (stop stopping stopped)	USPAT	OR	OFF	2004/11/03 15:28
S7	18727	memory adj controller	USPAT	OR	OFF	2004/11/03 15:31
S11	6693	S1 with (sav\$3 consumption off stop)	USPAT	OR	OFF	2004/11/03 16:23
S12	2	S11 with S7	USPAT	OR	OFF	2004/11/03 16:25
S13	3241	backup near power	USPAT	OR	OFF	2004/11/03 16:26
S14	846	(365/228,229).CCLS.	USPAT; USOCR	OR	OFF	2004/11/03 16:27
S16	99	S13 and S14	USPAT	OR	OFF	2004/11/03 16:27
S17	1	"6088762".PN.	USPAT; USOCR	OR	OFF	2004/11/03 16:29
S18	1	"6084813".PN.	USPAT; USOCR	OR	OFF	2004/11/03 16:29
S19	1	"6084813".PN.	USPAT; USOCR	OR	OFF	2004/11/03 16:32
S20	1	"6018802".PN.	USPAT; USOCR	OR	OFF	2004/11/03 16:33
S21	1	"5659208".PN.	USPAT; USOCR	OR	OFF	2004/11/03 16:33
S22	1	"5640357".PN.	USPAT; USOCR	OR	OFF	2004/11/03 16:35
S23	1	"5590082".PN.	USPAT; USOCR	OR	OFF	2004/11/03 16:35
S24	1	"5465236".PN.	USPAT; USOCR	OR	OFF	2004/11/03 16:42
S25	1	"5430681".PN.	USPAT; USOCR	OR	OFF	2004/11/03 16:42
S26	1	"4979171".PN.	USPAT; USOCR	OR	OFF	2004/11/03 16:43

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	19597	memory adj control	USPAT	OR	OFF	2004/11/03 13:55
S2	236330	power adj supply	USPAT	OR	OFF	2004/11/03 13:55
S3	213	S1 with S2	USPAT	OR	OFF	2004/11/03 13:55
S5	25	S3 with battery	USPAT	OR	OFF	2004/11/03 13:58
S6	1	S5 with self\$1refresh	USPAT	OR	OFF	2004/11/03 14:30
S9	341	pull\$1 adj down adj resistance	USPAT; JPO	OR	OFF	2004/11/03 14:32
S10	15	S9 with memory	USPAT; JPO	OR	OFF	2004/11/03 14:38
S11	120018	power adj4 (sav\$3 down failure shut off cut)	USPAT; JPO	OR	OFF	2004/11/03 14:44
S12	10003	S11 with (controller cpu processor)	USPAT; JPO	OR	OFF	2004/11/03 14:45
S13	1732	S12 with (memory ram dram)	USPAT; JPO	OR	OFF	2004/11/03 14:46
S14	232	S13 with battery	USPAT; JPO	OR	OFF	2004/11/03 15:07
S16	12	S14 with refresh	USPAT; JPO	OR	OFF	2004/11/03 14:47
S27	41205	("365").CLAS.	USPAT; USOCR	OR	OFF	2004/11/03 15:09
S28	846	(365/228,229).CCLS.	USPAT; USOCR	OR	OFF	2004/11/03 15:10
S31	228	S28 and resistance	USPAT	OR	OFF	2004/11/03 15:11
S32	212	S31 and memory	USPAT	OR	OFF	2004/11/03 15:12
S33	180	S32 and switch\$3	USPAT	OR	OFF	2004/11/03 15:12
S34	98	S33 and battery	USPAT	OR	OFF	2004/11/03 15:12
S35	27	S34 and refresh	USPAT	OR	OFF	2004/11/03 15:13